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**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q82454

Jean-Pierre BOUZIDI

Allowed: December 20, 2005

Appln. No.: 10/502,354

Group Art Unit: 2646

Confirmation No.: 5721

Examiner: SWERDLOW, Dan

Filed: July 23, 2004

For: DIFFERENTIAL INPUT STAGE FOR ELECTRONIC EQUIPMENT, COMPRISING  
MEANS FOR REDUCING INTERFERENCE CAUSED BY A VOLTAGE OR  
CURRENT IN COMMON MODE

**SUBMISSION OF REPLACEMENT DRAWINGS**

**MAIL STOP ISSUE FEE**

**ATTN: OFFICIAL DRAFTSPERSON**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Submitted herewith please find 1 sheet of replacement drawings in compliance with  
37 C.F.R. § 1.84. The Examiner is respectfully requested to acknowledge receipt of these  
drawings.

The submitted drawings incorporate the proposed drawing changes indicated by the  
Examiner's Amendment in the Notice of Allowance dated December 20, 2005.

Respectfully submitted,

David J. Cushing

Registration No. 28,703

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WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Date: January 9, 2006